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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/710,453	07/13/2004	Kerry Bernstein	BUR920040024US1	4452
30449	7590	11/22/2005	EXAMINER	
SCHMEISER, OLSEN + WATTS			TAN, VIBOL	
3 LEAR JET LANE			ART UNIT	
SUITE 201			PAPER NUMBER	
LATHAM, NY 12110			2819	

DATE MAILED: 11/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.



## **DETAILED ACTION**

### ***Election/Restrictions***

1. Restriction to one of the following inventions is required under 35 U.S.C. 121:
  - I. Claims 1-22, drawn to electronic digital logic circuitry, classified in class 326, subclass 81.
  - II. Claims 23-32, drawn to design and analysis of circuit or semiconductor mask, classified in class 716, subclass 1.

The inventions are distinct, each from the other because of the following reasons:

2. Inventions I and II are unrelated. Inventions are unrelated if it can be shown that they are not disclosed as capable of use together and they have different modes of operation, different functions, or different effects (MPEP § 806.04, MPEP § 808.01). In the instant case the integrated circuit comprising one or more logic stages is not intended to be used together with the method of designing a monotonically CMOS circuit comprising synthesizing, analyzing and replacing. The group I recites an integrated circuit comprising one or more logic stages whereas the group II recites a method of designing a monotonically operated CMOS circuit. Thus, the groups I and II have different modes of operation.
3. Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.


4. Because these inventions are distinct for the reasons given above and the search required for Group I is not required for Group II, restriction for examination purposes as indicated is proper.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Vibol Tan whose telephone number is (571) 272-1811. The examiner can normally be reached on Monday-Friday (7:00 AM-4:30 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on (571) 272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

  
**VIBOL TAN**  
**PRIMARY EXAMINER**